# Space transformer & Ultra fine pitch PCB Solution

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Session 3

## **PCB Market Overview**

#### **Global PCB Market Overview**

- Market Size: Approximately \$70 billion as of 2023
  - South Korea Market Size: Approximately \$10 billion as of 2023
- Growth Rate: Expected CAGR of 3~5%
- Applications
  - Consumer Electronics, Telecommunications, Automotive Electronics,

Medical Devices, Aerospace and Military, Industrial Equipment, etc.



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**Technological changes in semiconductor testing** 





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## **Challenges for fine pitch PCBs**

### Fine-pitch solutions up to 0.27mm pitch

- MLB(Multi-layer board)
  - Precision etching process
  - Uniform plating thickness
  - Small machining drill process
  - Thermal expansion of materials

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- Multibook process

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PCB Thickness : 7.6mmT
PTH (Plated Through Hole) : 3.8mmT



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## **Challenges for fine pitch PCBs**

#### Fine-pitch solutions up to 40um pitch

- STO-ML(Space Transformer Organic-Multi Layer)
  - Space transformer PCB between PLB and Probe Head (or Socket)
  - STO-ML is the exclusive TSE brand of MLO



## **Application of Vertical Probe Card**

#### **Structure Overview**



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## **Space Transformer Organic-Multi Layer**

### **Fabrication Capability**

- STO
  - Pitch : Up to 80um
  - Stack Up : 16-n-16
  - Micro Via : 30um
  - C4 Pad Size : 60um
- STO-ML
- Pitch : Up to 40um
- Stack Up : 3(ML)-16-n-16
- Micro Via : 18um
- C4 Pad Size : 30um





## **Space Transformer Organic-Multi Layer**

#### **Material Overview**

- Low Dielectric Constant (Dk)
  - STO : 3.19@10GHz / ML : 3.1@10GHz
- Low Dissipation Factor (Df)
  - STO : 0.008@10GHz / ML : 0.018@10GHz
- Thermal characteristics
  - High Tg (TMA Thermomechanical Analysis : STO 230°C / ML 155°C)
  - Low CTE Coefficient of Thermal Expansion : 9~10ppm/°C





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## **Space Transformer Organic-Multi Layer**

#### **Key Process Technologies**

- Build Up Layer Lamination
  - The alignment and adhesion between layers are crucial
- Fine Patterning and Etching
  - Ensuring high-frequency characteristics through impedance matching
- Via fill Plating and Stacked via Alignment
  - Via plating should be free of voids and cracks, and alignment is critical
- Testing and Quality Control



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## **Space Transformer Organic-Multi Layer**

#### **Reliability Management**

- Reliability Test
  - Solder Floating (288°C @ 10sec)
  - Thermal Shock Cycling Test (125°C↔-55°C @ 100Cycles)
  - Constant Thermal & Humidity Test (40°C @ 90%)
- Test Verification (IPC-6012D)
  - BBT (Electrical Check)
  - Cross Section (Mechanical Check)



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# Space Transformer Organic-Multi Layer

### **Reliability Management**

- Cross Section
  - Delamination
  - Crack
  - Void
  - Via fill plating
  - Stacked via alignment
  - etc.







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## **Space Transformer Organic-Multi Layer**

#### Signal Integrity of High Speed IO

CPU, GPU, Chipset, AP, Microcontroller, ASIC, FPGA, etc.



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## **Space Transformer Organic-Multi Layer**

#### **Simulation based PCB Design**

Simulation 

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- S-Parameter analysis for signal integration
- Z-Parameter analysis for power integration

ТҮРЕ	Software	Maker
EM Tool	PowerSI	Cadence
	Slwave	ANSYS
	PowerDC	Cadence
	HFSS	ANSYS
Circuit Tool	ADS	Keysight
	Designer SI	ANSYS





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## **TestConX Korea 2024**

## **Space Transformer Organic-Multi Layer**

### Signal Integrity of STO-ML

- PCIe Gen5 32Gbps
  - Tx ↔ Rx: Trace length 30mm & AC Coupling Capacitor
  - Insertion Loss: less than -3dB / Return Loss : more than -15dB
  - Impedance: Differential pair  $100\Omega \pm 10\%$



#### VNA (Vector Network Analyzer)

- Maker : Keysight
- Model: N5224A
- Bandwidth :10MHz ~ 43.5GHz
- Probe Spec : 50GHz RF probe





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## **Space Transformer Organic-Multi Layer**

### **SMT (Surface Mount Technology)**

- Selection of solder balls considering pitch and pad size
- Application of a jig for PCB alignment and maintaining flatness
- Management of SMT profiles for each product to ensure solder joint quality



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## Summary

#### **Challenges for fine-pitch PCBs**

- High layer fine-pitch support through multi-book process
- Ultra fine-pitch support through space transformer process

#### Simulation based PCB Design

Ensuring signal integrity and power integrity characteristics

Surface mount technology for fine-pitch PCBs

Optimal temperature profile, solder ball selection and flatness control



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